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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re patent application of  
Toshiharu Furukawa et al**

**Examiner: Loke, Steven Ho Yin**

**Serial No: 09/944,665**

**Art Unit: 2811**

**Filed: August 31, 2001**

**Docket: BUR919990305US1**

**For: Vertical Dual Gate Field Effect  
Transistor**

**Dated: April 5, 2004**

**Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, D.C. 20231**

**AMENDMENT UNDER 37 C.F.R. §1.111**

Sir:

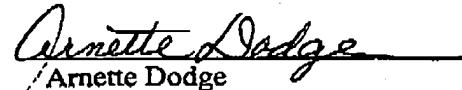
In reply to the Office Action dated Dec. 3, 2003, Applicants respectfully request reconsideration of the outstanding objections and rejections, and reexamination of the present application in light of the following amendments and remarks.

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**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: April 5, 2004

  
Arnette Dodge

Serial No. 09/944,665

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